



Software security, secure programming

Information Flow, Non Interference, Sandboxing ...

Master M2 Cybersecurity

Academic Year 2024 - 2025

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#define SIZE 4 // public PIN size
#define MAX TRIES // maximal tries number
unsigned int triesLeft = MAX_TRIES ; // tries counter
boolean checkPIN (char[] inputPin) {
 // No more than triesLeft attempts
 if (triesLeft < 0) return false : // no authentication
 // Main comparison
 for (short i=0; i < SIZE; i++)
   if (inputPin[i] != secretPin[i]) {
      triesLeft-- ;
      return false: // no authentication
 // Comparison is successful
 triesLeft = maxTries :
 return true : // authentication is successful
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functional property:

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\texttt{checkPIN(inputPIN)} \Leftrightarrow \texttt{inputPin[0..SIZE} - 1] = \texttt{secretPin[0..SIZE} - 1]
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What about confidentiality of the secret PIN?

- ▶ should be protected against reverse-engineering
- should be protected against (side-channel) information leakage

Information levels

Several information levels "coexist" inside an execution platform:

- ► from different users (including root/admin/...)
- from different processes/threads/applets (e.g., web browser)
- from different input sources (trusted/untrusted, confidential/public)
- etc.
- ⇒ a lattice, with lower and higher information level values
- → Avoid unexpected interferences between cross level information flows ?

Security properties to preserve/enforce

confidentiality:

→ no information leakage from higher to lower data "no write down", "no read up"

integrity:

→ no information rewritting from lower to higher data "no write up", "no read down"

Examples:

- sensitive shared plateform level data (e.g., caches, etc.)
- sensitive OS level data (e.g., passwords, resource management, etc.)
- external data, owned by other users/threads
- sensitive internal application data (e.g., crypto keys, nonces, etc.)
- sensitive program execution level memory locations (e.g., canaries, return adresses, etc.)

Attacker model

- ▶ knows the code (executable → assembly, source ?)
- ▶ **observe** outputs + low variables+ part of the execution plateform . . .
- controls inputs + low variables
- may observe other side-channels
- ⇒ may direct program execution through controlled inputs
 - to produce/increase leakage of higher values
 - to break integrity (of higher data, of code execution, etc).

Rk: could even elaborate interactive/adaptive multi-steps attack strategies!

How information may flow?

- ► Inside a single-threaded application, use/def variable dependencies
 - data-flow (direct/explicit) through assigments
 - control-flow (indirect/implicit) through <u>if</u>, <u>while</u>, ... statements, exceptions, etc.
- ► Through side channels
 - execution time, termination
 - power consumption
 - micro-architecture level (shared) resources: caches, intruction pipelines, branch prediction, etc.
 - others?
- ► Between concurent/remote processes/threads
 - sockets, remote calls
 - shared resources (and race conditions!)

Protection against (unwanted) information flows

- Hardware mechanisms (enclaves, etc.)
- OS primitives and access control mechanisms, Virtual Machines
- Language level facilities and libraries (crypto, etc.)
- Coding rules (input sanitization, constant-time programming)
- Compiler options (to enforce protection at the executable code level)
- some tools ...
 - ► static analysis: type systems --- fix-point computations
 - → not decidable, (over-)-approximation, not complete
 - runtime instrumentation/monitoring techniques (taint tags, extra checks) → not sound (may miss existing flows)

But:

protection mechanisms always rely on a TCB (Trusted Computing Base)

Non Interference: a general definition (1/4)

- more precisely: no <u>influence</u> of variable/statement of one class to another influence = read and/or write and/or execute
- numerous applications in security:
 - confidentiality/integrity
 - taint analysis (e.g., user-controled vulnerability exploitability)
 - ▶ side-channels through shared resources (execution time, cache, ...)
 - no use of unitialized variables (undefined behavior)
 - etc.

Non Interference: confidentiality (2/4)

No influence from data/statement of class H to data/statement of class L

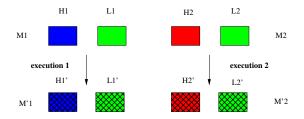
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- a variable partition in 2 classes H and L
- \blacktriangleright memory states M1=(L1, H1) and M2=(L2, H2) s.t. L1 \equiv L2 and H1 \neq H2

Then, any executions from M1 and M2 lead to memory states M'1=(I'1, H'1) and M'2=

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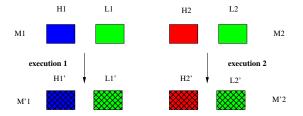
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Rk:

- do not take termination into account (see later)
- hyper property (models are sets of execution sequences, not single ones ...)

Non Interference: integrity (3/4)

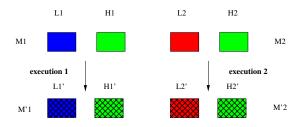
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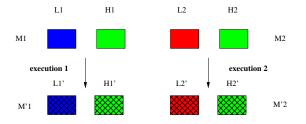


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A variable/statement is tainted at a program location if its value/execution is influenced by a user input

- taint source = "user input channel" (keyboard, network, filesystem, etc.)
- ► taint sink = (unwanted) user-controled vulnerable variable/statement
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- ▶ some variables/statements labelled as TSo (taint source) or TSi (taint sink)
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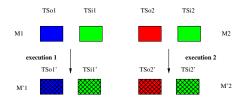
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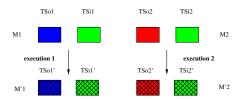
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Rk:

- not need to take non-termination into account . . .
- hyper property (models are sets of execution sequences, not single ones ...)

In the following ...

1. Information flow within sigle-threaded applications (see E. Poll' slides)

2. Side channels (next slides below)

3. Sandboxing and access control (see E. Poll' slides)

Information leakage through side channels

Are these programs secure?

In both cases.

- some additional information about the secret is leaked by the time or the instruction cache
- by interacting iteratively with the application, the adversary is able to improve his knowledge

Side channels

Information leakage through:

- ▶ (implicit) shared resources: caches, hidden registers, etc.
- physical observations: time, power consuption, etc.

A same cause: the use of **high** variables to control

- ▶ (global) memory accesses (e.g, arrays) → data cache attacks
- ▶ execution control flow ~ instruction cache or branch prediction attacks
- time-dependent (assembly level) instructions

constant-time¹ programming:

a set of coding rules to protect against such attacks . . .

See for instance:

- A beginner's guide to constant-time cryptography
- (Intel) Guidelines for Mitigating Timing Side Channels Against Cryptographic Implementations
- Some Cryptocoding rules

¹not related to **time complexity!**

Some research directions regarding side channels

- Quantifying the information leakage (Quantitative Information Flow) is always leaking one single (same!) bit of a crypto key less critical than leaking only once the whole key?
- Quantifying the "control level" of an attacker how much can she/he influence the execution, at which cost?
- ▶ Distinguish **regular** *vs* **unwanted** outputs when computing the leakage *e.g.*, *password checking may* (at least!) return a boolean value
- Improve automatic detection of side channel information flows (hyper-property checking)
- Automatic code transformation to constant time mode, dedicated programming languages, etc.